

ABSTRACT OF THE DISCLOSURE

In accordance with an embodiment of the present invention, a processor may receive a supply voltage provided by an external voltage regulator. The processor may include a voltage sensor, the output of which may be a control signal to indicate if the supply voltage is above or below a target value. This target value may be adjusted by the processor in accordance with a power management policy. The control signal may be provided to the external voltage regulator to adjust the supply voltage accordingly.

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